**Name:** Calxeda

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¶1: **Startup Failure Post-Mortems 2015 First Update (8/15/2015)**

### ¶2: Calxeda

¶3: Title: [Low power won’t bag ARM the server crown](http://www.theregister.co.uk/2014/01/08/calxeda_postmortem/)  
¶4: Title Link: <https://www.theregister.com/2014/01/08/calxeda_postmortem/>

¶5: Product: [Calxeda](https://www.cbinsights.com/company/calxeda)  
¶6: Product Link: <https://www.cbinsights.com/company/calxeda>

¶7: In [Calxeda’s] case, we moved faster than our customers could move. We moved with tech that wasn’t really ready for them – ie, with 32-bit when they wanted 64-bit. We moved when the operating-system environment was still being fleshed out – [Ubuntu Linux maker] Canonical is all right, but where is Red Hat? We were too early.

## **¶8:** Founded Year

¶9: 2008

## **¶10:** Stage

¶11: Dead | Dead

## **¶12:** Total Raised

¶13: $104.75M

## **¶14:** About Calxeda

¶15: Calxeda, formerly Smooth-Stone, brings new performance density to the datacenter on an attractive power foot print by leveraging ultra-low power processors as used on mobile phones as a foundation for its technology. Calxeda will make it possible for datacenter managers to increase the density of computer resources while reducing the need for power, space and cooling.

## **¶16:** Calxeda Headquarter Location

¶17: 7000 North Mopac Expressway Suite 250

¶18: Austin, Texas, 78731,

¶19: United States

¶20:

# **¶21: Low power WON'T bag ARM the server crown. So here's how to upset Intel**

¶22: Ex-veep spills beans on where chip upstart Calxeda went wrong Jack Clark in San Francisco Wed 8 Jan 2014 // 10:29 UTC

¶23:

¶24: Title Link: <https://www.theregister.com/2014/01/08/calxeda_postmortem/>

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¶26:   
¶27: **EXCLUSIVE** In the last days of 2013, Calxeda, the ambitious startup that hoped to design ARM processors for data-center servers, imploded.

¶28: Now *El Reg* has sifted its ashes, and pulled out some advice for the silicon upstart's contemporaries.

¶29: The [demise of Calxeda](https://www.theregister.com/2013/12/19/calxeda_shutdown/) caused many to ponder the viability of general-purpose ARM-powered computing in the data center, especially given Intel's twin threats of its [custom chip business](https://www.theregister.com/2013/11/06/intel_custom_chips_update/) and a newfound dedication to shrinking the power consumption of its [Avoton](https://www.theregister.com/2013/09/03/intel_avoton_atom_server_chip_preview/) server chips.

¶30: Calxeda's plan was to [design chip packages](https://www.theregister.com/2012/10/17/calxeda_arm_server_chip_roadmap/) that combined its network fabric electronics and other controllers with processor cores licensed from ARM; the resulting blueprints would turned into working silicon by outside fabs, and used in kit from the likes of HP. The startup's low-power [ECX-1000](http://www.calxeda.com/ecx-1000-techspecs/) used the quad-core 32-bit Cortex-A9 architecture; the [ECX-2000](http://www.calxeda.com/ecx-2000-family/) used the Cortex-A15.

¶31: But 64-bit parts, desirable for enterprise-grade workloads, weren't scheduled for release until 2014, a date the company just couldn't reach before the lights were switched off.

¶32: Last night, *El Reg* approached Karl Freund, Calxeda's former veep of marketing, and he told us about what other ARM-powered startups can do to avoid Calxeda's fate, and why major adoption of the chips is likely, but not for the power-consumption reasons promulgated by the press.

¶33: "In high tech, we are all trained by the years of the dot-com boom to think that being first to market is critical," said Freund, who is now an independent analyst and consultant on ARM servers.

¶34: "In [Calxeda's] case, we moved faster than our customers could move. We moved with tech that wasn't really ready for them – ie, with 32-bit when they wanted 64-bit. We moved when the operating-system environment was still being fleshed out - [Ubuntu Linux maker] Canonical [is all right](https://www.theregister.com/2013/09/12/canonical_ubuntu_landscape_arm_server/), but where is Red Hat? We were too early."

¶35: Calxeda's folly, Freund reckoned, was that it came out early enough to ignite a huge amount of interest from the industry and coverage in the media, but was too early to satisfy key customers with serious spending power.

¶36: Though large-scale operators such as Google and Facebook are all interested in ARM-driven servers – a senior Facebook executive joined Calxeda's board [in October](https://www.theregister.com/2013/10/18/facebook_frankovsky_calxeda_board/) – they are all going to wait until 64-bit ARMv8 packages are available, Freund reckoned, and will likely want some kind of software ecosystem to be present as well.

¶37: Chips stuffed with [64-bit ARM cores](https://www.theregister.com/2011/10/28/arm_holdings_arm_v8/) are expected to come along in the first months of this year, though Freund believes it will be in 2015 that the industry adopts the architecture in a big way.

¶38: In the year between release and adoption, in a sense we'll witness a Mexican standoff: the IT world's superstars won't run production systems on computers until they're demonstrated reliably running critical software. Someone will have to crack and take a shot on the platform before others invest time and money in testing and validate their applications for wide ARMv8 deployment.

¶39: "The big guys, they'll all experiment with ARM, many of them will deploy ARM, but not until first-generation 64-bit at best," Freund said.

¶40: "These guys have advanced technology strategists so they're always looking at what's next on the horizon. We have had deep, deep discussions with all of them about our technology and about our roadmap. They're all going to wait for 64-bit.

¶41: "Red Hat will not have [an ARM] RHEL until 2015 at the earliest. If you're an Amazon you're not going to stand up a big farm of ARM servers if there's nobody running a big load of software on ARM."

### ¶42: The reason ARM will be successful is not performance per watt

¶43: ARM-compatible processors tend to end up in handheld devices, and thus aren't as battery hungry as their x86 cousins. In these eco-friendly times, the suggestion you could run an ARM server farm off, say, a solar panel array was enticing for companies with hefty electricity bills.

¶44: If power-sipping [64-bit ARMv8](http://www.arm.com/products/processors/armv8-architecture.php) server chips have a chance of serious adoption in the data center come 2015, there's a good chance Intel will at that time be fielding some capable Atom processors with close-to-ARM milliwatt-per-MHz ratings thanks to Chipzilla's advanced manufacturing expertise.

¶45: Freund says this is "a very cogent argument," and claimed somewhat heretically that "the reason ARM will be successful is not lower performance per watt."

¶46: A minor performance-per-watt advantage (and by 2015 [all signs point](https://www.theregister.com/2012/08/13/xeon_vs_calxeda_arm_apache_bench/) to it being minor at best) is not great enough to get people to switch architectures, he said.

¶47: Instead, the flexibility of the ARM architecture will be vital – specifically, the freedom to license a streamlined ARM core design and bolt on whatever custom hardware is needed to perform or accelerate a particular task. This approach contrasts Intel's beefy but general-purpose packages.

¶48: Just as smartphone makers turn to system-on-chips with touchscreen-driving GPUs and wireless networking built alongside battery-friendly processor cores, server manufacturers should be able to pick up parts that strongly glue multiple cores to gigabit ethernet and high-end SATA controllers.

¶49: "It's the ability to customize around ARM's intellectual property, which you cannot do with Intel intellectual property," Freund told us. "If you're Google looking at some kind of acceleration, or Sandia Labs looking at very specific algorithms you want to tune for, or you like the [Calxeda] fabric and want disaggregated resource models – that's why you pick ARM."

¶50: This lines up with *El Reg's* [own analysis](https://www.theregister.com/2013/12/16/google_intel_arm_analysis/) in December, and the thoughts of a well-placed source within the semiconductor industry.

¶51: Though Intel has an early-stage custom x86 chip business that already works with eBay and Facebook, Freund believes the pace at which a company can either buy and modify ARM chips, or contract to a third party like a Phoenix-rebirthed Calxeda to do so, will beat Intel every time.

¶52: "Either customize for a very large account or customize for a very specific workload or market segment," he said. "My advice to other ARM vendors - I wish them all the luck - is you either have to add significant added-value or very, very low costs."

¶53: Pay attention, Applied Micro's "X-Gene" team, and AMD's "Seattle" crew, or else you risk a dim reception to your promises of future chippery. ®

### ¶54: Bootnote

¶55: Intel experimented with ARM-compatible system-on-chips called the Xscale family between 2002 and [2006](https://www.theregister.com/2006/06/28/intel_mobile_failure/), at which point it offloaded the tech to Marvell. Chipzilla pitched the processors at mobile phones, networking kit, disk controllers and similar embedded computing products. Calxeda [clearly thought](https://www.theregister.com/2010/11/16/calxeda_arm_server_chip/) bolting cores onto specialized hardware was the way to go: its co-founder Barry Evans once ran Intel's Xscale businesses.

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¶57: